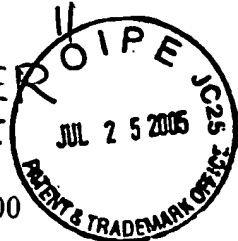


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Docket No.: 049657-0700

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PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Tsutomu YOSHIMURA, et al.

RESPONSE UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE

Application No.: 09/584,728

Customer No.: 20277

Filed: June 01, 2000

Confirmation No.: 5073

Group Art Unit: 2631

Examiner: Khanh C. Tran

Title: DIGITAL SYNCHRONOUS CIRCUIT FOR STABLY GENERATING OUTPUT  
CLOCK SYNCHRONIZED WITH INPUT DATA

**AMENDMENT UNDER 37 CFR 1.116**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The following Amendments and Remarks are submitted in response to the Office Action  
dated May 24, 2005, pursuant to the provisions of 37 C.F.R. § 1.116.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this  
paper.

**Remarks** begin on page 9 of this paper.